# Dr. Peter H. Hsieh Biography

## Biography

Dr. Hsieh has been a recognized entrepreneur for several successful Silicon Valley start-ups and held several executive positions for large semiconductor companies of US, Taiwan, China, and UK cumulating over 30 years of professional experience in IC design, software development, and semiconductor manufacturing.

Dr. Hsieh's career accomplishment includes building the world's first single chip CMOS RISC microprocessor under Dr. John Cocke, inventor of RISC architecture and recipient of US National Medal of Science & Technology and Turing Award, at IBM Watson Research Center; the CMOS gate array family for the first CMOS IBM compatible supercomputer. Dr. Hsieh was founder and founding team member of several successful Silicon Valley start-ups including the world's first Fabless IC design company, Chips and Technologies. Dr. Hsieh received "The Entrepreneur of the Year" Award in 1990. Dr. Hsieh received his Ph.D. in EECS from MIT and served as Media Lab's Advisory Board Director from 1984-1987.

Prior to joining ARM, Dr. Hsieh was R & D President for Freescale Semiconductor China Design Centers responsible for Industrial and Automotive MCU and application processors design and development. These Design Centers are among the largest and highest quality design centers in China. These design centers were responsible for over 80% of Freescale's industrial/automotive MCU design and development.

Dr. Hsieh is currently serving as ARM Taiwan General Manager. In this capacity, Dr. Hsieh works closely with Taiwan customers, ecosystem partners, universities, and government functions for mobile computing, automotive, medical devices and health care, green energy, IoT, and server markets. Dr. Hsieh established ARM's first Asia Design Center in Hsinchu, Taiwan in 2014.

## Education

Dr. Hsieh received **BSEE** degree from the **University of California, Berkeley**; **MSEE**, **Electrical Engineer**, and **Ph.D.** degrees from **Massachusetts Institute of Technology**. Dr. Hsieh also studied **MBA** curriculum as his Ph.D. minor at **Harvard** business school.

# Award

- **Best Thesis** award from MIT EECS Department in 1979.
- **"The Entrepreneur of the Year"** award in 1990 in Silicon Valley.
- Freescale Suzhou Design Center has been named **"Outstanding Research Institute"** by Jiangsu province and China MIIT, 2007, 2009, and 2011.
- Graduated from MIT with 5.0 GPA & awarded Assistant Professorship before graduation; Highest GPA from College of Engineering, UC Berkeley in 1974

# Professional Membership and Accomplishments

Dr. Hsieh is a member of **ITU, ISO, IMTC, IETF, IEEE, Phi Beta Kappa,** and **Eta Kappa Nu**. Dr. Hsieh served as a **Technical Advisor Board Director** for **MIT Media Laboratory** from 1984-1987. Dr. Hsieh also served as **Board Directors** of **CINA** and **CASPA**, two of the San Francisco Bay Area networking and semiconductor professional associations. He also served as **Board Director** for **China Semiconductor Association**. Dr. Hsieh has been an invited speaker at various major conferences.   He has authored and co-authored more than 100 technical papers and has been awarded numerous computer system and VLSI design related patents.

# Contact Information

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